IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.	:	10/618,237
Applicant	:	Gang Zhang et al.
Filed	:	July 11, 2003
Title	:	ANALOG INTEGRATED CIRCUIT LAYOUT DESIGN
Group Art Unit	:	2825
Examiner	:	Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT

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P.O. Box 1450
Alexandria, VA 22313-1450

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Deborah L. Medves
(Name of Person Mailing Papers)

Deborah L. Medves 12/30/03
Signature Date

this Information Disclosure Statement are enclosed.

Respectfully submitted,

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Attorney Docket Number	2879-030687

Sheet 1 of 3

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, cite and/or country where published.	T[2]
	1	R. HARJANI, R.A. RUTENBAR and L.R. CARLEY, "OASYS: A Framework For Analog Circuit Synthesis", IEEE Transactions On Computer-Aided Design, Vol. 8, No. 12, pp. 1247-1266, (December 1989).	
	2	M.G.R. DEGRAUWE, O. NYS, E. DIJKSTRA, J. RIJMENANTS, S. BITZ, B.L.A.G. GOFFART, E.A. VITTOZ, S. CSERVENY, C. MEIXENBERGER, G. VAN DER STAPPEN, and H. J. OGUEY, "IDAC: An Interactive Design Tool For Analog CMOS Circuits", IEEE Journal Of Solid State Circuits, Vol. Sc-22, No. 6, pp. 1106-1116, (December 1987).	
	3	H.Y. KOH, C.H. SEQUIN and R.R. GRAY, "OPASYN: A Compiler For CMOS Operational Amplifiers", IEEE Transactions On Computer-Aided Design, Vol. 9, No. 2, pp. 113-125 (February 1990).	
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	6	G.G.E. GIELEN and R.A. RUTENBAR, "Computer-Aided Design Of Analog And Mixed-Signal Integrated Circuits", Proceedings Of The IEEE, Vol. 88, No.12, pp. 1825-1852 (December 2000).	
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	9	U. CHOUDHURY and A. SANGIOVANNI-VICENTELLI, "Constraint Generation For Routing Analog Circuits", 27th ACM/IEEE Design Automation Conference, pp. 561-566, (June 1990).	
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	11	E. CHARBON, E. MALAVASI, D. PANDINI and A. SANGIOVANNI-VINCENTELLI, "Imposing Tight Specifications On Analog IC's Through Simultaneous Placement And Module Optimization", IEEE 1994 Custom Integrated Circuits Conference, pp. 525-528, (May 1994).	
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	18	J.M. COHEN, D.J. GARROD, R.A. RUTENBAR and L.R. CARLEY, "KOAN/ANAGRAM II: New Tools For Device-Level Analog Placement And Routing", IEEE Journal Of Solid-State Circuits, Vol. 26, No. 3, pp. 330-342, (March 1991).	
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	21	G. ZHANG, A. DENGI, and L.R. CARLEY, "Automatic Synthesis Of A 2.1GHz SiGe Low Noise Amplifier", IEEE RFIC Symposium, pp. 1-4 (June 2002).	

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